

DC 503 U. S. 570
09/827014



		Subclass
	Class	

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

Scanned

88

APPLICATION NO. 09/827014	CONT/PRIOR	CLASS 257	SUBCLASS 778	ART UNIT 12811 3822 EPA	EXAMINER AWOL-SATZ
------------------------------	------------	--------------	-----------------	----------------------------------	-----------------------

PLICANTS
- Sylvia Adae - Amorokoh
John Kresge
Voya Markayef
Thompson Young

Method and structure for economical high density chip carrier

TITLE **BEST AVAILABLE COPY**

PTO-2040
12/29

ISSUING CLASSIFICATION

Continued on Inside Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____				ISSUE FEE	
<input checked="" type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Primary Examiner)			Amount Due	Date Paid
				ISSUE BATCH NUMBER	
	(Legal Instruments Examiner)			(Date)	

WARNING:

The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Ferrit PTO-436A
(Rev. 8/92)

FILED WITH: DISK (CBF) FICHE CD-ROM

100% 電子化申請書

(Attached in pocket on right inside flap)